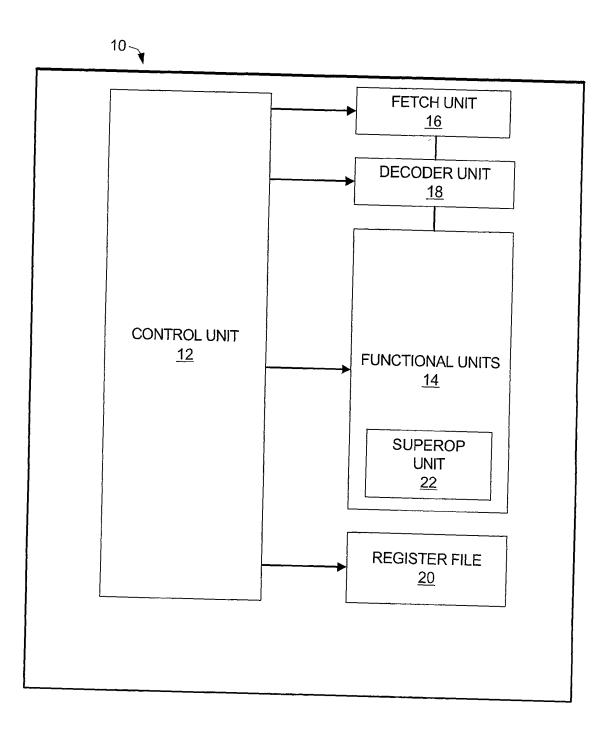
Processor

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FIG. 1

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FIG. 2

Sheet 2 of 5

Processor

Title: Combining Operations in a VLIW

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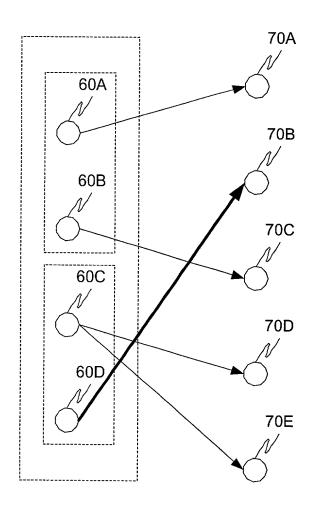


FIG. 3

Sheet 4 of 5 Title: Combining Operations in a VLIW

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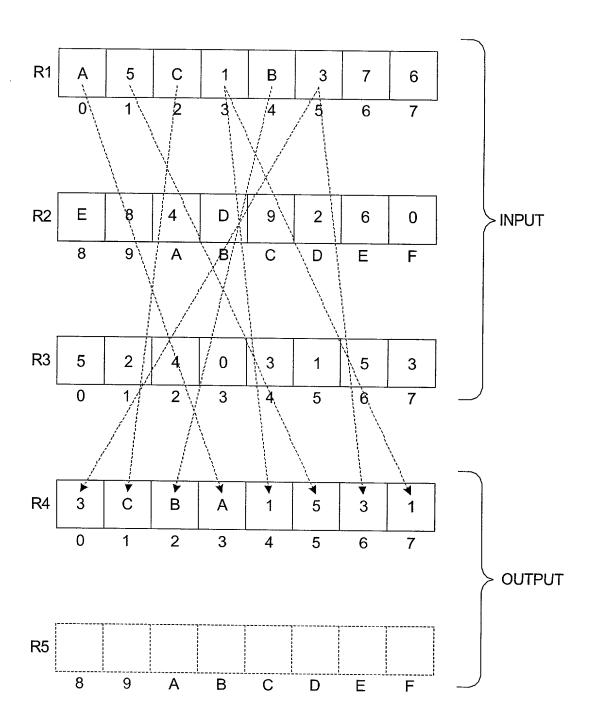


FIG. 4

Sheet 5 of 5

Title: Combining Operations in a VLIW

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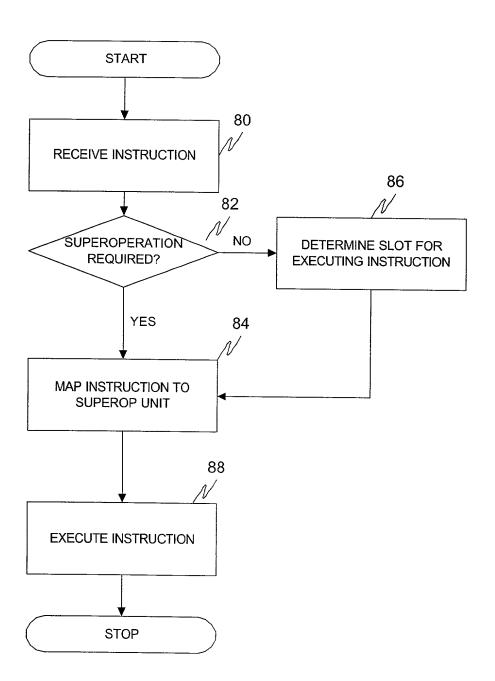


FIG. 5